

PLL83B

2013MAN001a

()
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PLL



Fig.1 PLL83B

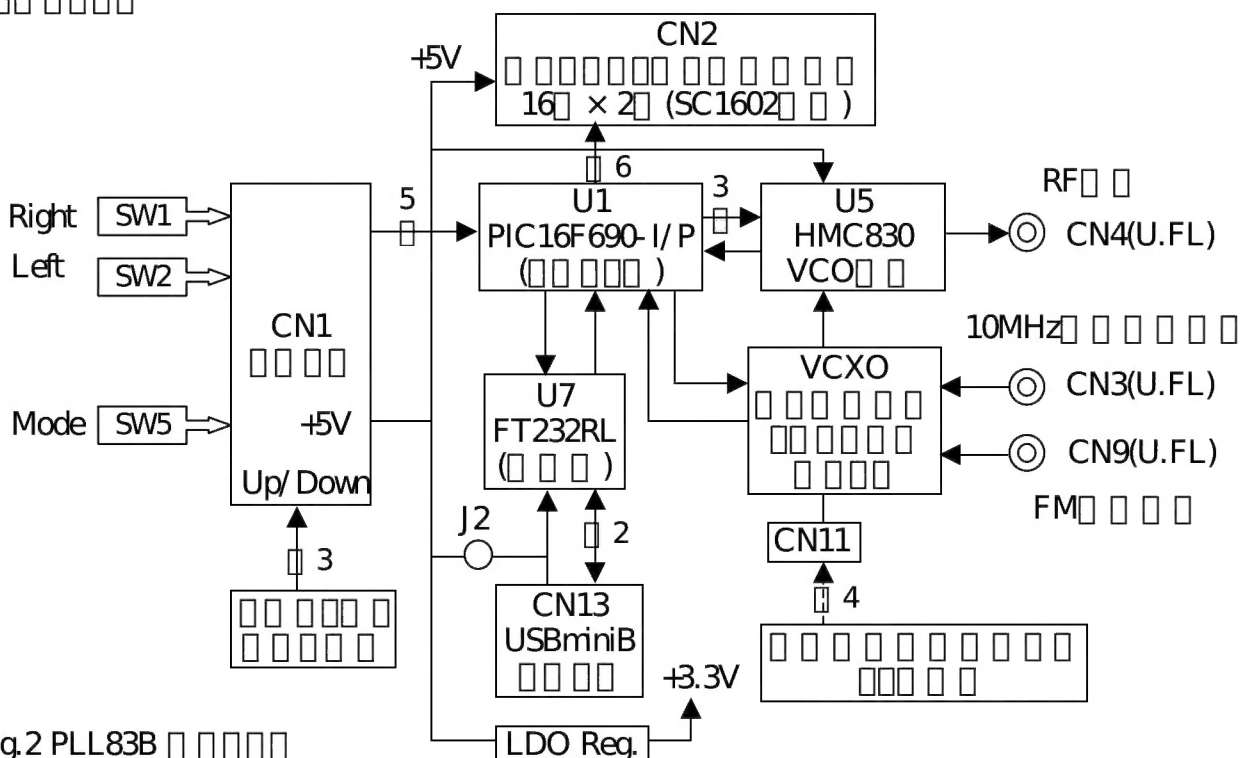


Fig.2 PLL83B

□ □ □ □	.DSB6062BE	8□
□ □ □ □	.ASB2611- 3E	4□
□ □ □ □	.CB2602E	4□

□ □ □ □ □ □ □ □ □ □ 4□ □ □ □ □ □ □ □ □ □ M3(□ □ □ □ □ □ □ □ □ □ M2.6)
 □ □ □ □ □ □ □ □ □ □ 4□ □ □ □ □ □ □ □ □ □ DSB-6062BE □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □
 81W×24H[mm] □ □ □ □ □ □ □ □ □ □ M3×□ □ □ □ □ 4mm

SW3 Up SW4 Down
CN1
PLL_AUX
CN1
(GND PIC)
RA0 RA5 V 0 +3.3V

[illegible][illegible]

A 5x10 grid of squares. The squares contain the following text (row by row, left to right):

- Row 1: 10 empty squares.
- Row 2: 10 empty squares.
- Row 3: 1st square contains 'CN3', 2nd-9th squares are empty, 10th square contains 'CN11'.
- Row 4: 1st square contains 'CN11', 2nd square contains 'CN11', 3rd square contains 'CN11', 4th square contains 'CN11', 5th square contains 'CN11', 6th square contains 'CN11', 7th square contains 'CN11', 8th square contains 'CN11', 9th square contains 'CN11', 10th square contains 'CN11'.
- Row 5: 10 empty squares.

1	TRIM	
2	/FREQ2	
3	LOCK	GND
4	+3.3V	CW
5	GND	CCW
6	PGD	
7	PGC	

Table.2 CN11: B7B-EH-A

□. □Check Encorder!!□□ □ □ □ □ □ □

```

00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000  SW1 SW5(CN1) OFF
00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000
00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000  Check Encoder!!
00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000  BCD_ch_mode
00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000
00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000

```

10 Sweep BCD_ch 0 0 ch 0 0 0 0 0 0

[illegible]

□ □ □ □ □ □ □ □ □ □

[illegible]

□□□□ □

6

2

13. □ □ □ □ □ □ □ □ □ □ □ □

U1(PIC16F690- I/ P) IC

PIC16F690- I/ P

D-sub

D-sub

5.0mm

U2(PIC16F690- I/ P) IC

PIC